

# C442N7 Jetson Orin Nano Embedded AI Edge Computer Product Brief (Preliminary) V2

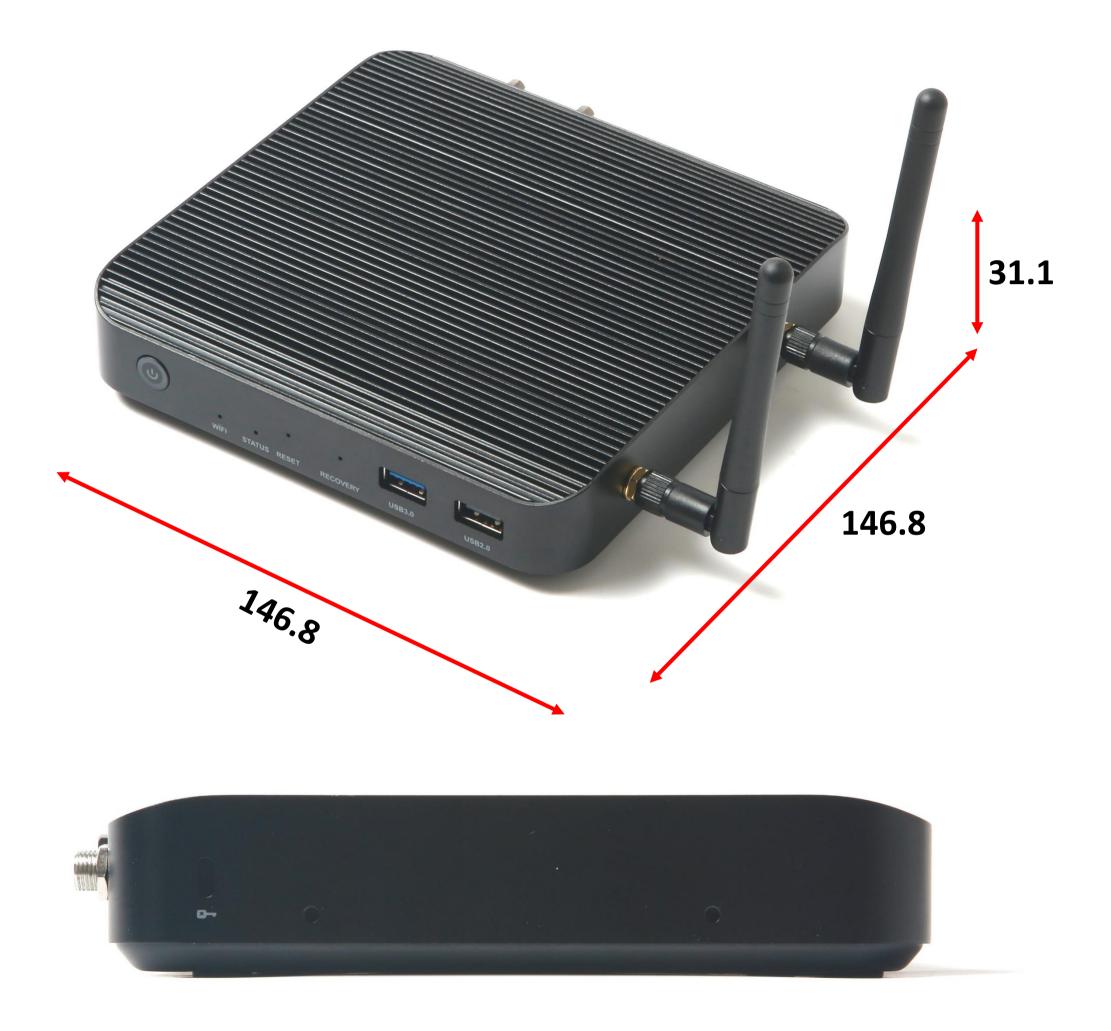
Created: April 19, 2023 Last updated: April 25, 2023 PM: L.M. Wong

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# **Document Update**

- Version 2:  $\bullet$ 
  - Corrected block diagram (swapped UART1 & 2)  $\bullet$
  - Updated operating temperature range lacksquare
  - Specified PCP P/N for M.2 NVMe SSD module and WIFI module  $\bullet$
  - Added 3D rendered drawing  $\bullet$
  - PCB version 004  $\bullet$
- Version 1: Initial draft (Support RS285/RS232 dual mode, WI-FI 6E)  $\bullet$

### **1. SYSTEM OUTLOOK**

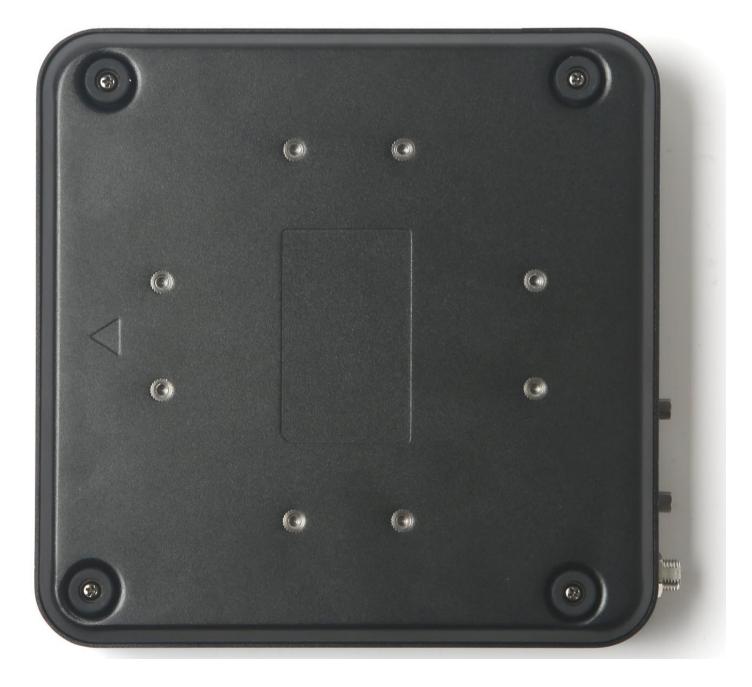


• Dimension unit: mm



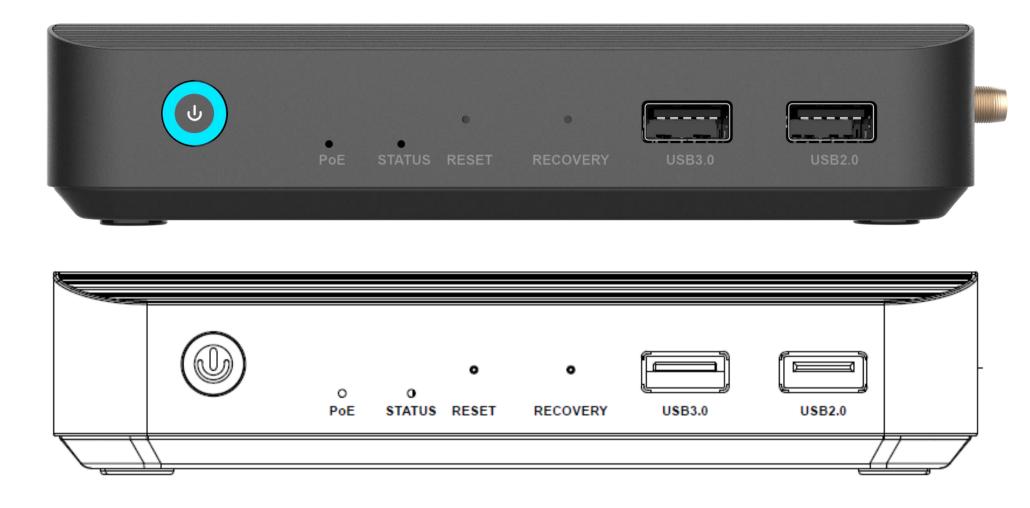


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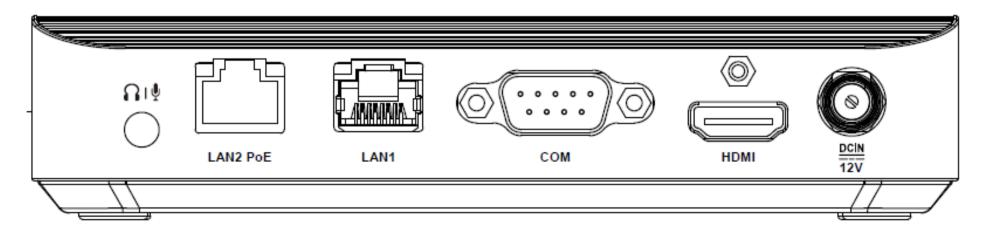


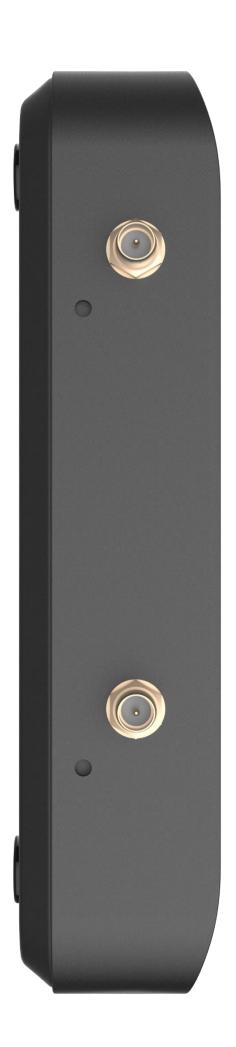
### **1. SYSTEM OUTLOOK**





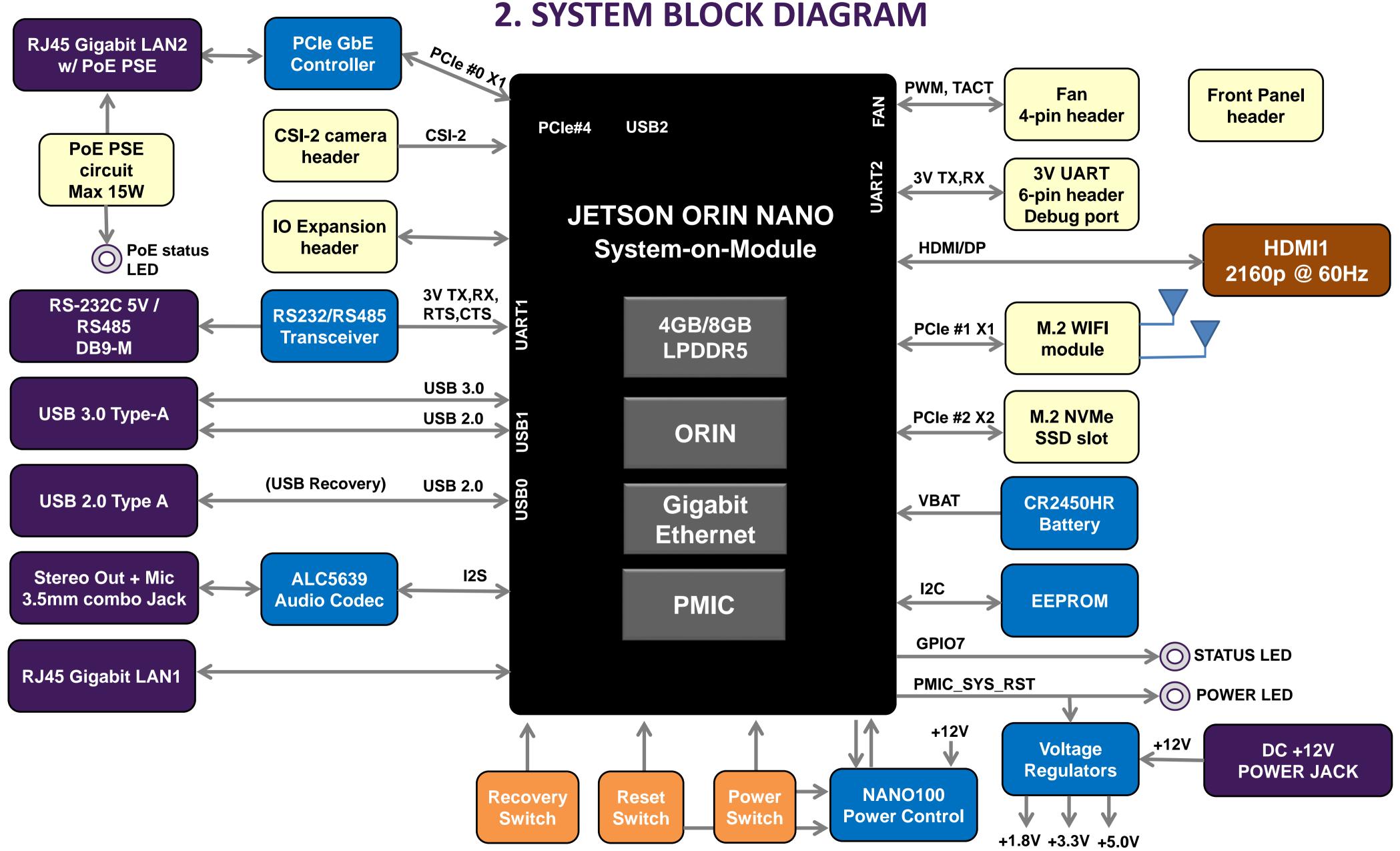












### **3. TECHNICAL SPECIFICATION**

- Based on NVIDIA Jetson Orin Nano 4GB/8GB module
- M.2 2280 NVMe PCIex2 slot. Pre-installed Phison 256GB SSD flash drive (PCP P/N 251-10102-0000F)
- 1 x HDMI 2.0 display output with locking screw
- I x USB 3.0 Type-A host port. 1 x USB 2.0 Type-A host port (support Jetson Force Recovery Mode)
- I x RS-232C/RS-485 dual mode serial port in DB-9M connector (TX, RX, RTS, CTS signals at 5V RS232 level)
- Intel Wi-Fi 6E AX210NGW 802.11ax tri-band Wi-Fi module (PCP P/N 251-15001-0200F)
- LAN1: Gigabit Ethernet based on Realtek RTL8119I-CG on Jetson module
- LAN2 PoE PSE: Gigabit Ethernet based on Realtek RTL8119I-CG on motherboard
  - 802.3af (802.3at Type 1) based on T.I. TPS23861 + MOSFET
  - Maximum continuous output power: 15.4W
  - Supported power classes: Class 1, 2, 3
  - Supported cabling: Category 5
  - Supported modes: Mode A, Mode B
- OPTION] 3.5mm stereo line-out & mono microphone combo jack
- Switches: Power push button, Reset tact switch (pin hole), Jetson Force Recovery tact switch (pin hole)
- LED indicators: Power, PoE, Status (User GPIO)
- Real-time clock battery: CR2450HR 3V 550mAh coin battery
- Power Input: 12VDC jack with locking screw. Max power consumption 35W (estimation) incl USB full loading. Bundle 60W 12V AC adapter
- Passive cooling case with mounting holes on both sides and Kensington lock hole. Dimension: 146.8mm (W) x 146.8mm (D) x 31.1mm (H)
- Operating temperature range: -20°C +45°C. Humidity 5 95% non-condensing
- FCC, CE, UL certified (pending). RoHS compliant
- Software: PC Partner Jetson Linux (L4T) patch, Splitter firmware update tool

### **3. TECHNICAL SPECIFICATION**

- Use Condition:
  - Duty cycle: 24 x 7
  - Indoor use
- Function not supported
  - Iteson Orin Nano, Jetson Orin NX: Do NOT support on-board and on-module eMMC flash storage
- Packing List:
  - Main unit x 1
  - Tri-band WIFI antenna x 2
  - Universal AC Adapter x 1. Output DC12V x 5A output with locking screw
  - AC power cord for US region (1.2M) x 1
  - AC power cord for EU region (1.2M) x 1

### **4. ORDERING INFORMATION**

| BOM                            |  |
|--------------------------------|--|
| 250-4B442-004EB<br>(tentative) | EMBEDDED JETSON ORI<br>WIFI, HDMI, GbE, PoE PS<br>Device identification:<br>HWID: 250-4B442-004<br>Jetson firmware versio  |
| 250-4B442-104EB<br>(tentative) | EMBEDDED JETSON ORI<br>WIFI, HDMI, GbE, PoE P<br>Device identification:<br>•HWID: 250-4B442-104<br>•Jetson firmware versio |
|                                | 250-4B442-004EB<br>(tentative)<br>250-4B442-104EB  |

#### Description

RIN NANO SYSTEM, 4GB LPDDR5, M.2 NVMe 256GB, AX210 PSE, RS232C/RS485, CR2450HR BATT, US/EU PLUGS, OEM BOX

4EB

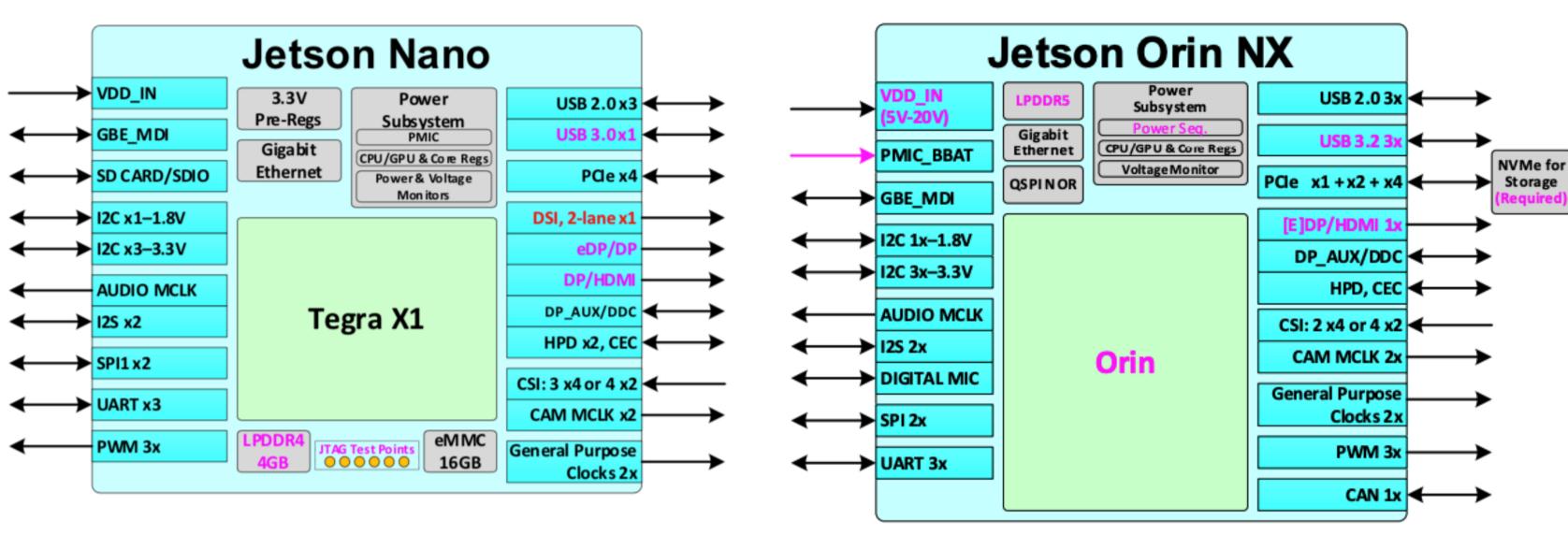
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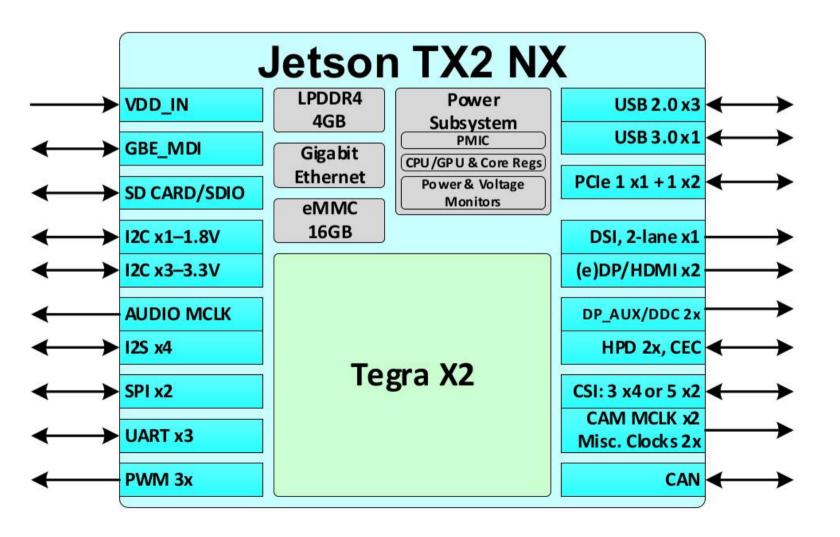
RIN NANO SYSTEM, 8GB LPDDR5, M.2 NVMe 256GB, AX210 PSE, RS232C/RS485, CR2450HR BATT, US/EU PLUGS, OEM BOX

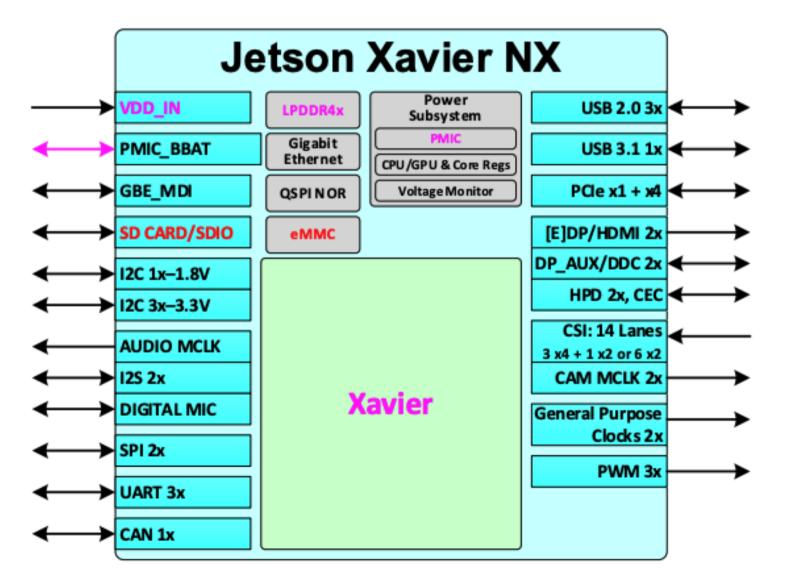
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### 5. BACKUP - JETSON NANO, TX2, XAVIER NX, ORIN NX, ORIN NANO BLOCK DIAGRAM

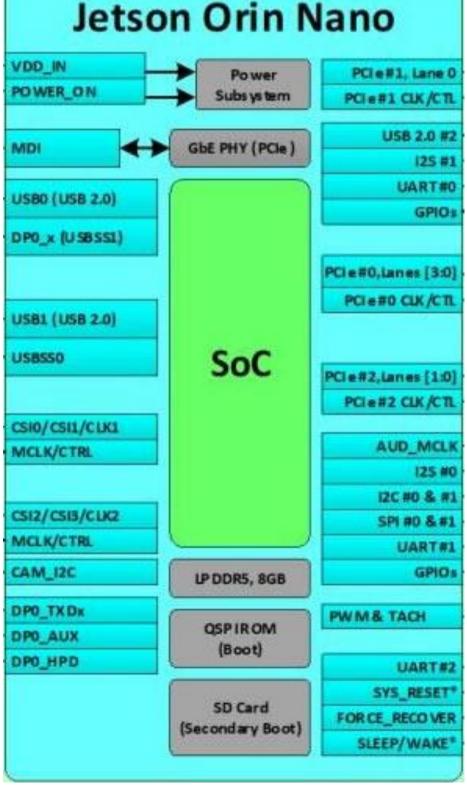






The interfaces or blocks that are supported only by one of the modules is highlighted in red.

The interface types that are supported on both modules but where the number of lanes and instances, voltage level, or access is different are highlighted in magenta.



### **5. BACKUP - JETSON NANO vs JETSON ORIN NX PINOUT DIFFERENCE**

#### Jetson Nano pinout

| Signal Name              | Pin #<br>Top<br>Odd | Pin #<br>Bottom<br>Even | Signal Name              | Signal Name                | Pin #<br>Top<br>Odd | Pin #<br>Bottom<br>Even | Signal Name               |
|--------------------------|---------------------|-------------------------|--------------------------|----------------------------|---------------------|-------------------------|---------------------------|
| GND                      | 1                   | 2                       | GND                      | PCIE0 RX0 P                | 133                 | 134                     | PCIE0 TX0 N               |
| CSI1_D0_N                | 3                   | 4                       | CSI0_D0_N                | GND                        | 135                 | 136                     | PCIE0_TX0_P               |
| CSI1_D0_P                | 5                   | 6                       | CSI0_D0_P                | PCIE0_RX1_N                | 137                 | 138                     | GND                       |
| GND                      | 7                   | 8                       | GND                      | PCIE0 RX1 P                | 139                 | 140                     | PCIE0 TX1 N               |
| RSVD                     | 9                   | 10                      | CSI0 CLK N               | GND                        | 141                 | 142                     | PCIE0 TX1 P               |
| RSVD                     | 11                  | 12                      | CSI0_CLK_P               | RSVD                       | 143                 | 144                     | GND                       |
| GND                      | 13                  | 14                      | GND                      | KEY                        | KEY                 | KEY                     | KEY                       |
| CSI1 D1 N                | 15<br>17            | 16<br>18                | CSI0 D1 N<br>CSI0 D1 P   | RSVD                       | 145<br>147          | 146<br>148              | GND<br>PCIE0 TX2 N        |
| CSI1 D1 P<br>GND         | 19                  | 20                      | GND                      | GND<br>PCIE0_RX2_N         | 149                 | 140                     | PCIE0_TX2_P               |
| CSI3 D0 N                | 21                  | 20                      | CSI2_D0_N                | PCIE0_RX2_P                | 151                 | 152                     | GND                       |
| CSI3 D0 P                | 23                  | 24                      | CSI2 D0 P                | GND                        | 153                 | 154                     | PCIE0 TX3 N               |
| GND                      | 25                  | 26                      | GND                      | PCIE0 RX3 N                | 155                 | 156                     | PCIE0 TX3 P               |
| CSI3_CLK_N               | 27                  | 28                      | CSI2_CLK_N               | PCIE0_RX3_P                | 157                 | 158                     | GND                       |
| CSI3 CLK P               | 29                  | 30                      | CSI2 CLK P               | GND                        | 159                 | 160                     | PCIE0 CLK N               |
| GND                      | 31                  | 32                      | GND                      | USBSS RX N                 | 161                 | 162                     | PCIE0 CLK P               |
| CSI3 D1 N                | 33                  | 34                      | CSI2 D1 N                | USBSS RX P                 | 163                 | 164                     | GND                       |
| CSI3_D1_P                | 35                  | 36                      | CSI2_D1_P                | GND                        | 165                 | 166                     | USBSS_TX_N                |
| GND                      | 37                  | 38                      | GND                      | RSVD                       | 167                 | 168                     | USBSS TX P                |
| DP0 TXD0 N               | 39                  | 40                      | CSI4 D2 N                | RSVD                       | 169                 | 170                     | GND                       |
| DP0_TXD0_P               | 41                  | 42                      | CSI4_D2_P                | GND                        | 171                 | 172                     | RSVD                      |
| GND                      | 43                  | 44                      | GND                      | RSVD                       | 173                 | 174                     | RSVD                      |
| DP0 TXD1 N               | 45                  | 46                      | CSI4 D0 N                | RSVD                       | 175                 | 176                     | GND                       |
| DP0 TXD1 P               | 47<br>49            | 48<br>50                | CSI4 D0 P<br>GND         | GND                        | 177<br>179          | 178<br>180              | MOD SLEEP*                |
| GND                      | 49<br>51            | 50                      |                          | PCIE_WAKE*                 | 179                 | 180                     | PCIE0_CLKREQ*             |
| DP0_TXD2_N<br>DP0_TXD2_P | 53                  | 54                      | CSI4_CLK_N<br>CSI4 CLK P | PCIE0_RST*<br>RSVD         | 183                 | 184                     | RSVD<br>GBE MDIO N        |
| GND                      | 55                  | 56                      | GND                      | I2C0 SCL                   | 185                 | 186                     | GBE MDIO N                |
| DP0_TXD3_N               | 57                  | 58                      | CSI4_D1_N                | 12C0_SDA                   | 187                 | 188                     | GBE LED LINK              |
| DP0_TXD3_P               | 59                  | 60                      | CSI4_D1_P                | 12C1_SCL                   | 189                 | 190                     | GBE_MDI1_N                |
| GND                      | 61                  | 62                      | GND                      | I2C1 SDA                   | 191                 | 192                     | GBE MDI1 P                |
| DP1_TXD0_N               | 63                  | 64                      | CSI4 D3 N                | I2S0 DOUT                  | 193                 | 194                     | GBE LED ACT               |
| DP1_TXD0_P               | 65                  | 66                      | CSI4_D3_P                | 12S0_DIN                   | 195                 | 196                     | GBE_MDI2_N                |
| GND                      | 67                  | 68                      | GND                      | 12S0 FS                    | 197                 | 198                     | GBE MDI2 P                |
| DP1 TXD1 N               | 69                  | 70                      | DSI D0 N                 | 12S0 SCLK                  | 199                 | 200                     | GND                       |
| DP1_TXD1_P               | 71                  | 72                      | DSI_D0_P                 | GND                        | 201                 | 202                     | GBE_MDI3_N                |
| GND                      | 73                  | 74                      | GND                      | UART1_TXD                  | 203                 | 204                     | GBE_MDI3_P                |
| DP1 TXD2 N               | 75                  | 76                      | DSI CLK N                | UART1 RXD                  | 205                 | 206                     | GPIO07                    |
| DP1 TXD2 P               | 77                  | 78                      | DSI CLK P                | UART1 RTS*                 | 207                 | 208                     | GPIO08                    |
| GND                      | 79<br>81            | 80<br>82                | GND<br>DOL D4 N          | UART1_CTS*                 | 209<br>211          | 210<br>212              | CLK_32K_OUT               |
| DP1_TXD3_N               | 83                  | 84                      | DSI_D1_N<br>DSI_D1_P     | GPIO09                     | 213                 | 212                     | GPIO10<br>FORCE RECOVERY* |
| DP1 TXD3 P<br>GND        | 85                  | 86                      | GND                      | CAM I2C SCL<br>CAM I2C SDA | 215                 | 214                     | GPIO11                    |
| GPIO0                    | 87                  | 88                      | DP0 HPD                  | GND                        | 217                 | 218                     | GPIO12                    |
| SPI0_MOSI                | 89                  | 90                      | DP0_AUX_N                | SDMMC_DAT0                 | 219                 | 220                     | I2S1_DOUT                 |
| SPI0 SCK                 | 91                  | 92                      | DP0 AUX P                | SDMMC DAT1                 | 221                 | 222                     | 12S1 DIN                  |
| SPI0_MISO                | 93                  | 94                      | HDMI CEC                 | SDMMC DAT2                 | 223                 | 224                     | I2S1 FS                   |
| SPI0_C S0*               | 95                  | 96                      | DP1_HPD                  | SDMMC_DAT3                 | 225                 | 226                     | I2S1_SCLK                 |
| SPI0_C S1*               | 97                  | 98                      | DP1_AUX_N                | SDMMC_CMD                  | 227                 | 228                     | GPIO13                    |
| UART0 TXD                | 99                  | 100                     | DP1 AUX P                | SDMMC CLK                  | 229                 | 230                     | GPIO14                    |
| UART0_RXD                | 101                 | 102                     | GND                      | GND                        | 231                 | 232                     | I2C2 SCL                  |
| UART0_RTS*               | 103                 | 104                     | SPI1_MOSI                | SHUTDOWN_REQ*              | 233                 | 234                     | I2C2_SDA                  |
| UART0 CTS*               | 105                 | 106                     | SPI1 SCK                 | PMIC BBAT                  | 235                 | 236                     | UART2 TXD                 |
| GND                      | 107                 | 108                     | SPI1 MISO                | POWER EN                   | 237                 | 238                     | UART2 RXD                 |
| USB0_D_N                 | 109<br>111          | 110                     | SPI1_CS0*                | SYS_RESET*                 | 239                 | 240<br>242              | SLEEP/WAKE*               |
| USB0_D_P<br>GND          | 111                 | 112<br>114              | SPI1_CS1*<br>CAM0 PWDN   | GND<br>GND                 | 241<br>243          | 242                     | GND<br>GND                |
| USB1 D N                 | 115                 | 114                     | CAMO PWDN<br>CAMO MCLK   | GND                        | 245                 | 244                     | GND                       |
| USB1_D_P                 | 115                 | 118                     | GPIO01                   | GND                        | 243                 | 240                     | GND                       |
| GND                      | 119                 | 120                     | CAM1 PWDN                | GND                        | 249                 | 250                     | GND                       |
| USB2 D N                 | 121                 | 122                     | CAM1 PWDN<br>CAM1 MCLK   | VDD IN                     | 251                 | 252                     | VDD IN                    |
| USB2 D P                 | 123                 | 124                     | GPIO02                   | VDD IN                     | 253                 | 254                     | VDD IN                    |
| GND                      | 125                 | 126                     | GPI003                   | VDD IN                     | 255                 | 256                     | VDD IN                    |
| GPI004                   | 127                 | 128                     | GPIO05                   | VDD IN                     | 257                 | 258                     | VDD IN                    |
| GND                      | 129                 | 130                     | GPI006                   | VDD IN                     | 259                 | 260                     | VDD IN                    |
| PCIE0_RX0_N              | 131                 | 132                     | GND                      |                            |                     |                         |                           |

| DP0 | TXE  |
|-----|------|
| DP0 | TXE  |
|     | GND  |
| DP0 | TXE  |
| DP0 | _TXE |
|     | GND  |
| DP0 | TXE  |
| DP0 | TXE  |
|     | GND  |
| DP0 |      |
| DP0 | _TXC |
|     |      |

|            |               | I   |
|------------|---------------|-----|
| GND        | MODULE_ID     | 217 |
| SDMMC_DAT0 | PCIE2_RST*    | 219 |
| SDMMC_DAT1 | PCIE2_CLKREQ* | 221 |
| SDMMC_DAT2 | PCIE3_RST*    | 223 |
| SDMMC_DAT3 | PCIE3_CLKREQ* | 225 |
| SDMMC_CMD  | PCIE3_CLK_N   | 227 |
| SDMMC_CLK  | PCIE3_CLK_P   | 229 |

### Jetson Orin NX pinout difference (blue color)

| D0_N | USBSS1_RX_N | 39 | 40 | CSI4_D2_N  | PCIE2_RX0_N   |
|------|-------------|----|----|------------|---------------|
| D0_P | USBSS1_RX_P | 41 | 42 | CSI4_D2_P  | PCIE2_RX0_P   |
| )    | GND         | 43 | 44 | GND        | GND           |
| D1_N | USBSS1_TX_N | 45 | 46 | CSI4_D0_N  | PCIE2_TX0_N   |
| D1_P | USBSS1_TX_P | 47 | 48 | CSI4_D0_P  | PCIE2_TX0_P   |
| )    | GND         | 49 | 50 | GND        | GND           |
| D2_N | USBSS2_RX_N | 51 | 52 | CSI4_CLK_N | PCIE2_CLK_N   |
| D2_P | USBSS2_RX_P | 53 | 54 | CSI4_CLK_P | PCIE2_CLK_P   |
| )    | GND         | 55 | 56 | GND        | GND           |
| D3_N | USBSS2_TX_N | 57 | 58 | CSI4_D1_N  | PCIE2_RX1_N   |
|      |             |    |    |            | (PCIE3_RX0_N) |
| D3_P | USBSS2_TX_P | 59 | 60 | CSI4_D1_P  | PCIE2_RX1_P   |
|      |             |    |    |            | (PCIE3_RX0_P) |

| 64 | CSI4_D3_N | PCIE2_TX1_N<br>(PCIE3_TX0_N) |
|----|-----------|------------------------------|
| 66 | CSI4_D3_P | PCIE2_TX1_P<br>(PCIE3_TX0_P) |
| 68 | GND       | GND                          |
| 70 | DSI_D0_N  | RSVD                         |
| 72 | DSI_D0_P  | RSVD                         |
| 74 | GND       | GND                          |
| 76 | DSI_CLK_N | RSVD                         |
| 78 | DSI_CLK_P | RSVD                         |
| 80 | GND       | GND                          |
| 82 | DSI_D1_N  | RSVD                         |
| 84 | DSI_D1_P  | RSVD                         |
| 86 | GND       | GND                          |
| 88 | DP0_HPD   | RSVD                         |
| 90 | DP0_AUX_N | RSVD                         |
| 92 | DP0_AUX_P | RSVD                         |

## 5. BACKUP - JETSON MODULE OPN, PRODUCT LIFE CYCLE, AI PERFORMANCE

| Jetson Module                | Ordering P/N       | Available through | AI Performance    | MSRP (USD) |
|------------------------------|--------------------|-------------------|-------------------|------------|
| Jetson AGX Orin 64GB         | 900-13701-0050-000 | January 2028      | 275 TOPS          | 1599       |
| Jetson AGX Orin 32GB         | 900-13701-0040-000 | January 2028      | 200 TOPS          | 899        |
| Jetson Orin NX 16GB          | 900-13767-0000-000 | January 2028      | 100 TOPS          | 599        |
| Jetson Orin NX 8GB           | 900-13767-0010-000 | January 2028      | 70 TOPS           | 399        |
| Jetson Orin Nano 8GB         | 900-13767-0030-000 | January 2028      | 40 TOPS           | 299        |
| Jetson Orin Nano 4GB         | 900-13767-0040-000 | January 2028      | 20 TOPS           | 199        |
| Jetson AGX Xavier 64GB       | 900-82888-0050-000 | January 2025      | 32 TOPS           | 1299       |
| Jetson AGX Xavier            | 900-82888-0040-000 | January 2025      | 32 TOPS           | 899        |
| Jetson AGX Xavier Industrial | 900-82888-0080-000 | July 2031         | 30 TOPS           | 1249       |
| Jetson Xavier NX 16GB        | 900-83668-0030-000 | January 2026      | 21 TOPS. 2x NVDLA | 499        |
| Jetson Xavier NX             | 900-83668-0000-000 | January 2026      | 21 TOPS. 2x NVDLA | 399        |
| Jetson Nano                  | 900-13448-0020-000 | January 2027      | 472 GFLOPS        | 99         |
| Jetson TX2 NX                | 900-13636-0010-000 | February 2026     | 1.33 TFLOPS       | 149        |
| Jetson TX2                   | 900-83310-0001-000 | January 2025      | 1.33 TFLOPS       | 399        |
| Jetson TX2i                  | 900-83489-0000-000 | April 2028        | 1.26 TFLOPS       | 749        |

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